



CIT Case No. 1917
JPL Case No. C-16984
Pocket No. JPL 87-008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Inventor: Larry A. Bergman

Serial No. 103,192

Group No. 237

Filed: 1 October 1987

For: OPTICALLY INTRACONNECTED COMPUTER EMPLOYING
DYNAMICALLY RECONFIGURABLE HOLOGRAPHIC OPTICAL
ELEMENT

Examiner: K. KRIESS

AMENDMENT

Hon. Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

In response to the outstanding Official Office Action dated 9 June 1989, please
amend this application as follows:

In the Claims:

1. (Amended) In a computer including a memory for holding a sequence of instructions to be executed, logic for accessing the instructions in sequence, logic for determining for each instruction [the] a function to be performed and [the] an effective address thereof, and logic for executing each instruction, the improvement [comprising] wherein:

a) the logic for executing instructions [comprising] comprises a plurality of individual elements on a common support substrate optimized to perform certain logical sequences employed in executing instructions; and additionally comprising,

b) element selection logic means connected to the logic determining the function to be performed for each instruction for determining [the] a class of each function and for causing the instruction to be executed by those said elements which perform those associated said logical sequences affecting the instruction execution in an optimum manner, said element selection logic means portion for causing the instruction to be executed by those said elements which perform those associated said logical

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GROUP 230

CREDIT

FEE VALUE
ACCOUNTABILITY
DEPOSIT ACCOUNT NO.

FEE CODE VALUE
203 12

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1 217

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215.00 CK

48.00 CK